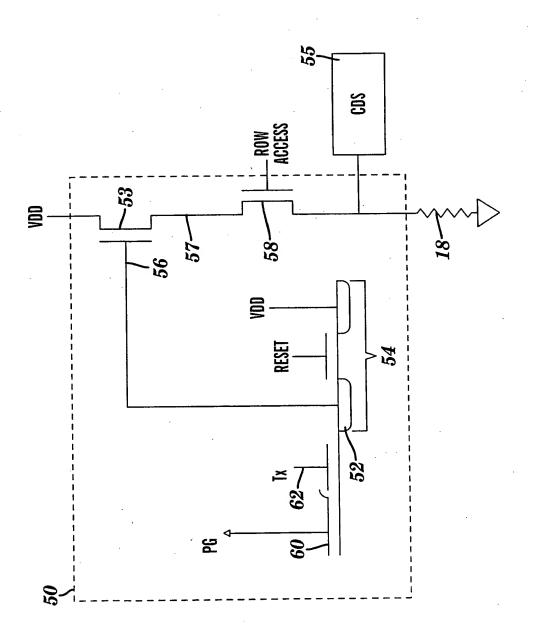
Title:Video Bus for High Speed . . . Inventor(s): Jeffrey Zarnowski Serial No.: 09/768,124 Attorney Docket: 711.001PA Sheet: 1/12



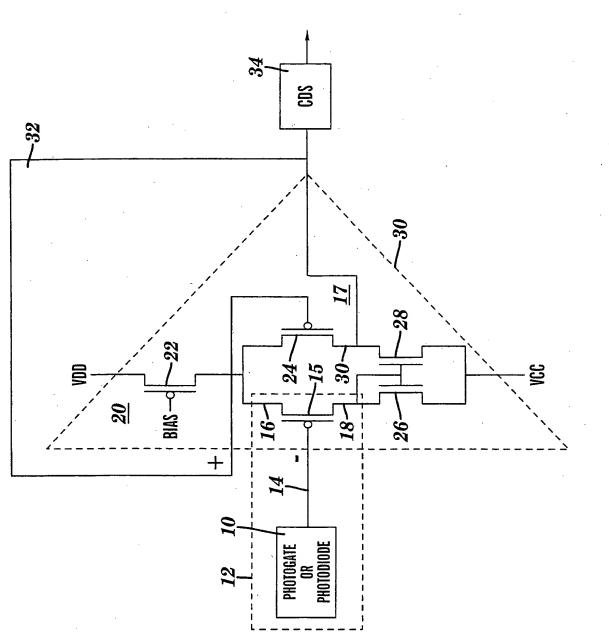
1/12

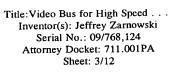
6633029













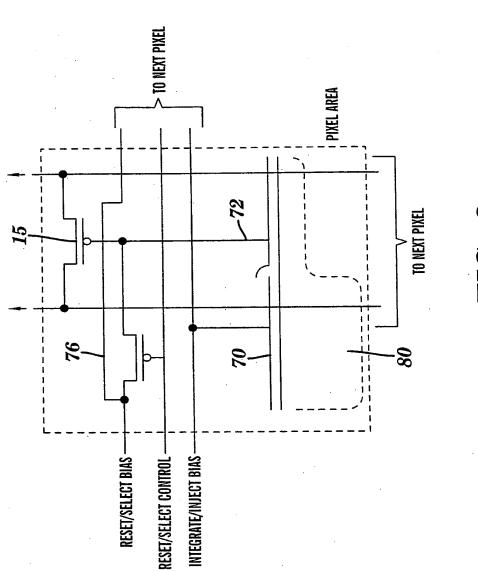
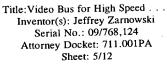


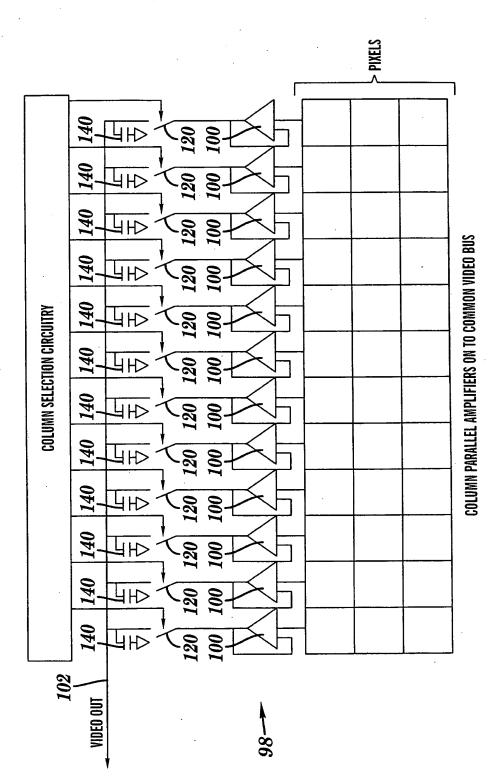
FIG. 4













Title:Video Bus for High Speed . . . Inventor(s): Jeffrey Zarnowski Serial No.: 09/768,124 Attorney Docket: 711.001PA Sheet: 6/12

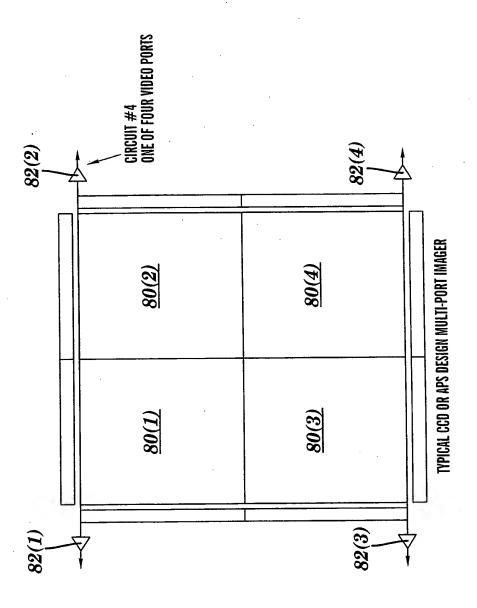
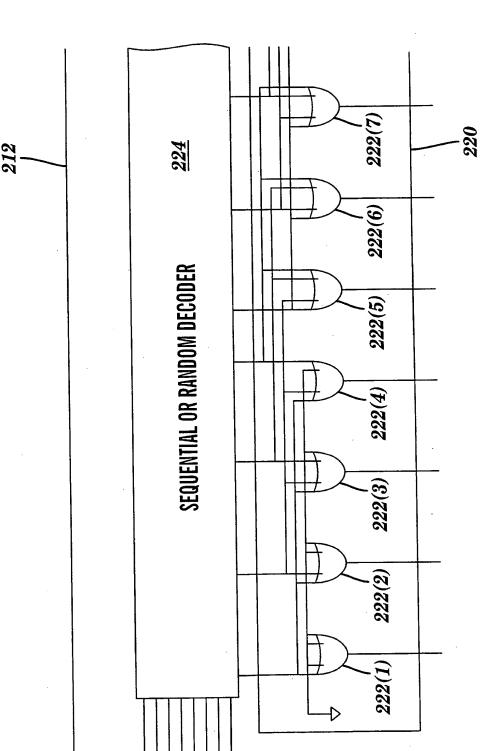


FIG. 6

Title: Video Bus for High Speed . Inventor(s): Jeffrey Zarnowski Serial No.: 09/768,124 Attorney Docket: 711.001PA Sheet: 7/12 7/12 FIG. 7A -200(1)-202(1)**1**204) -214(1)-216(4) -214(3) -216(3) -214(2)-214(4) -216(2)~212 220 224 210 $20\dot{6}(1)$ | $20\dot{6}(3)$ | $20\dot{6}(5)$ | $20\dot{6}(7)$ | $20\dot{6}(n)$ 206(2) | 206(4) | 206(6) | 206(8)COS SEQUENTIAL OR RANDOM DECODER COS PVS BUS THAT ELIMINATES MULTI-PORTS FOR HIGH SPEED BLACK AND WHITE $232(1)\ 232(2)\ 232(4)$ COS PIXEL ARRAY CDS Ses COS COS SOS 218(7)--(9)817218(3)-218(8) 218(5)218(1) -218(4)218(2)208(3)208(1)-208(4)-208(2)-208(n)VIDEO PROCESSING BLOCK DEMULTIPLEXOR OF Differential video VIDEO -(9)877228(4) -228(5)-228(2) -228(3)-228(7)-228(8)-236234-228(1)

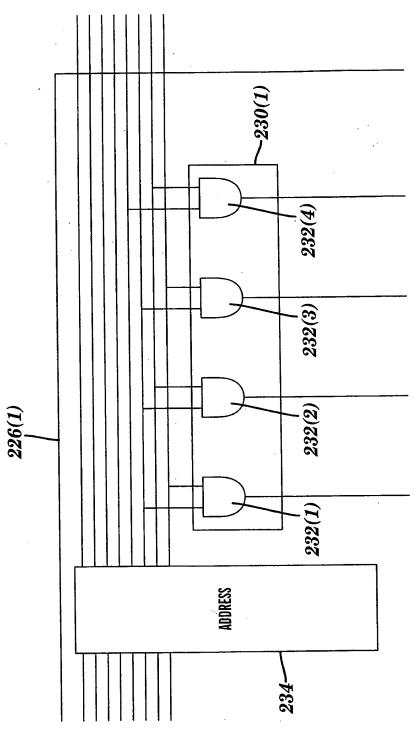
Title:Video Bus for High Speed . . . Inventor(s): Jeffrey Zarnowski Serial No.: 09/768,124 Attorney Docket: 711.001PA Sheet: 8/12

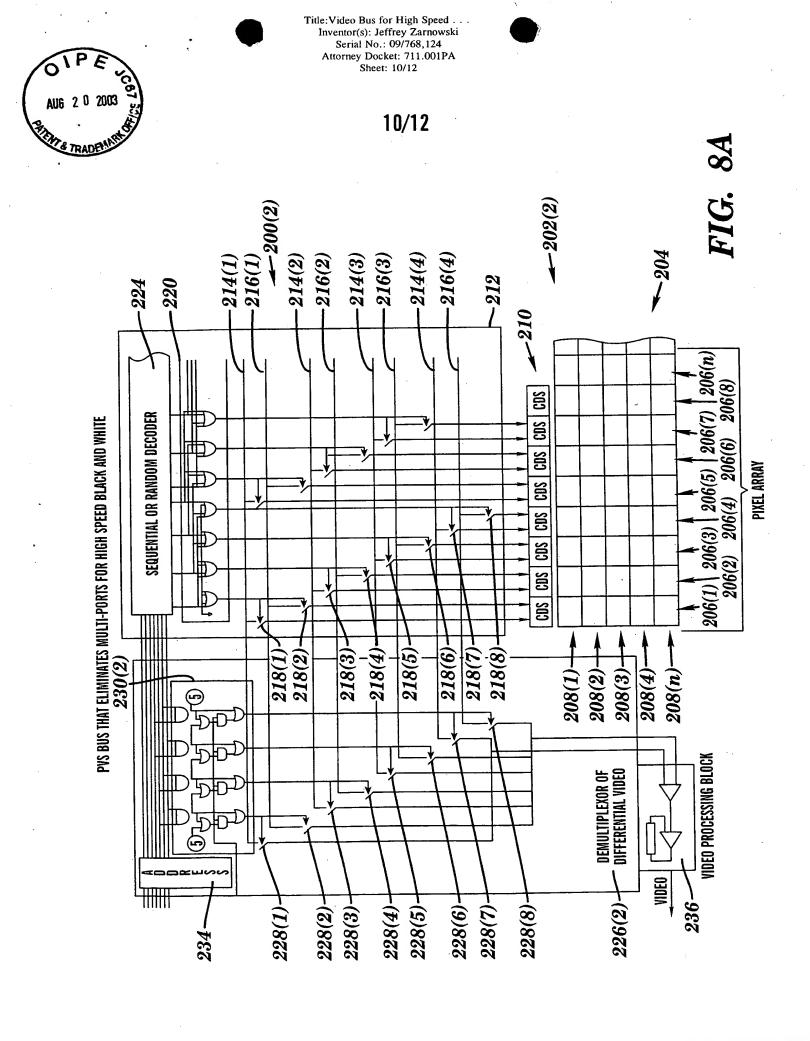




Title:Video Bus for High Speed . . . Inventor(s): Jeffrey Zarnowski Serial No.: 09/768,124
Attorney Docket: 711.001PA
Sheet: 9/12









Title:Video Bus for High Speed . . . Inventor(s): Jeffrey Zarnowski Serial No.: 09/768,124
Attorney Docket: 711.001PA
Sheet: 11/12

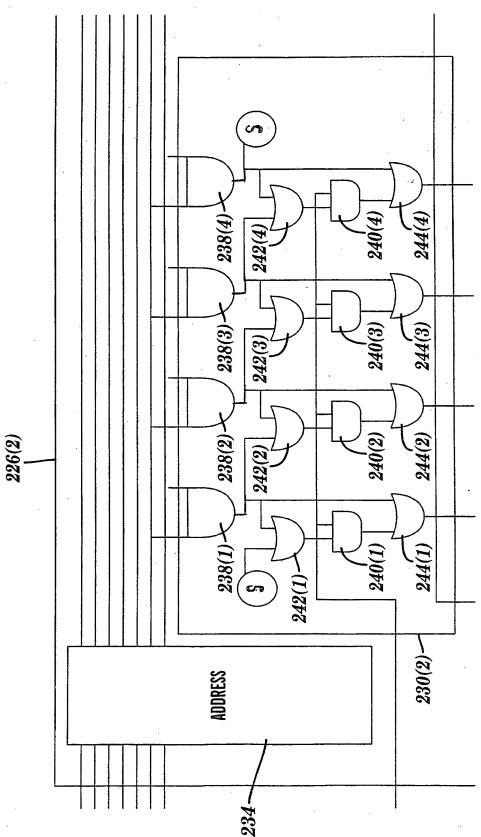


FIG. 8B



Title:Video Bus for High Speed . . . Inventor(s): Jeffrey Zarnowski Serial No.: 09/768,124
Attorney Docket: 711.001PA
Sheet: 12/12

